R E M A R K S

Careful review and examination of the subject application are noted and appreciated.

INTERVIEW SUMMARY

Applicants' representative (Chris Maiorana) and Examiner Chou held a telephone interview on September 10, 2007. Independent claim 1 was discussed. No agreement was reached.

SUPPORT FOR THE CLAIM AMENDMENTS

Support for the claim amendments may be found in the specification, for example, in FIG. 4, on page 9, lines 1-4 and on page 12, lines 12-16. Thus, no new matter has been added.

CLAIM REJECTIONS UNDER 35 U.S.C. §102

The rejection of claims 1-2 and 9-10 under 35 U.S.C. §102(e) as being anticipated by Aweya et al. '651 (hereinafter Aweya) has been obviated by amendment and should be withdrawn.

Aweya concerns a technique for synchronizing clocks in a network (Title).

In contrast, claim 1 of the present invention provides a method for controlling the frequency of oscillation of a local clock signal comprising the steps of (A) generating the local clock signal in response to a first control signal, (B) generating the

first control signal in response to one of a plurality of adjustment signals selected in response to a second control signal, and (C) generating the second control signal in response to a comparison between a local timestamp and an external timestamp. The second control signal selects one of a plurality of adjustment signals when a difference between the local time stamp and the external timestamp is outside a predefined margin. No adjustment signals are selected when the difference is within the predefined margin. The predetermined margin is configurable. Claims 9 and 10 have similar limitations. Aweya does not disclose or suggest all of these limitations.

In particular, Aweya fails to disclose (i) a second control signal that selects one of a plurality of adjustment signals when a difference between the local time stamp and an external timestamp is outside a predefined margin, (ii) no adjustment signals are selected when the difference is within the predefined margin, and (iii) the predetermined margin is configurable, as presently claimed. Although Aweya discloses a method of synchronizing clocks in a network, Aweya fails to disclose such an adjustment based on a predefined margin, as presently claimed. Aweya also fails to disclose that no adjustment signals are selected when the difference is within the predetermined margin, as presently claimed. Aweya also fails to disclose that the predetermined margin is configurable. Therefore,

Aweya does not disclose or suggest the presently claimed invention and the rejection should be withdrawn.

CLAIM REJECTIONS UNDER 35 U.S.C. §103

The rejection of claims 3-6 and 13-16 under 35 U.S.C. §103(a) as being unpatentable over Aweya et al. 651' (hereinafter Aweya) in view of Gervais et al., U.S. Patent Application Pub. No. 2006/0062195, (hereinafter Gervais) is respectfully traversed and should be withdrawn. Claims 3-6 and 13-16 depend, directly or indirectly, on claims 1, 9, or 10, which are now believed to be allowable.

The rejection of claims 7 and 8 under 35 U.S.C. §103(a) as being unpatentable over Aweya in view of Gervais and in further view of Skerritt et al., U.S. Patent Application Pub. No. 2006/0109059, (hereinafter Skerritt) is respectfully traversed and should be withdrawn. Claims 7-8 depend, directly or indirectly, on claim 1, which is now believed to be allowable.

The rejection of claims 11 and 12 under 35 U.S.C. §103(a) as being unpatentable over Aweya in view of Skerritt is respectfully traversed and should be withdrawn. Claims 11 and 12 depend, directly or indirectly, on claim 10, which is now believed to be allowable.

Newly presented claims 17 and 18 are independently patentable over the cited references. The references do not teach

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or suggest a predetermined margin that is configurable in response to a control signal received from a computer readable medium in a satellite set-top box. The references also do not teach or suggest a method that generates an error message if the local time stamp and the external time stamp are not within the predetermined margin.
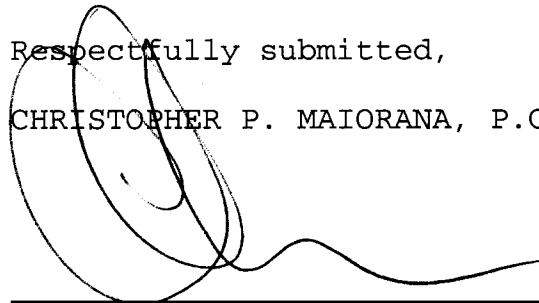
As such, the presently claimed invention is fully patentable over the cited references and the rejection should be withdrawn.

Accordingly, the present application is in condition for allowance. Early and favorable action by the Examiner is respectfully solicited.

The Examiner is respectfully invited to call the Applicants' representative between the hours of 9 a.m. and 5 p.m. ET at 586-498-0670 should it be deemed beneficial to further advance prosecution of the application.

If any additional fees are due, please charge Deposit Account No. 12-2252.

Respectfully submitted,

CHRISTOPHER P. MAIORANA, P.C.

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Christopher P. Maiorana
Registration No. 42,829

Dated: <u>September 18, 2007</u>

c/o Lloyd Sadler
LSI Corporation

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